

LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
<i>HEN</i>	1. 09/162,852	9/29/98	T.J. Gabara and S.A. Mujtaba, "Multi-Input Comparator."		
<i>HEN</i>	2. 6,191,623	02/20/01	Gabara	327/71	09/29/98
<i>HEN</i>	3. 6,097,768	08/01/00	Janesch et al.	375/80	11/2/97
<i>HEN</i>	4. 5,877,718	03/02/99	Andoh et al.	341/155	03/24/97
<i>HEN</i>	5. 5,790,063	08/04/98	Koifman et al.	341/155	12/16/96
<i>HEN</i>	6. 5,636,252	06/03/97	Patel et al.	375/345	12/15/95
<i>HEN</i>	7. 5,606,625	02/25/97	Dallavalle et al.	384/107	05/26/94
<i>HEN</i>	8. 5,576,837	11/19/96	Strolle et al.	386/33	08/26/94
<i>HEN</i>	9. 5,563,531	10/08/96	Meyer	327/3	09/15/94
	10. 4,767,949	08/30/88	Mayweather, III	327/80	05/01/87

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO
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OTHER DOCUMENTS

EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
<i>HEN</i>	1.	J. Montanaro et al., "A 160-MHz, 32-b, 0.5-W CMOS RISC Microprocessor," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, pp. 1703-1714, 1996.
<i>HEN</i>	2.	J.-C. Kim et al., "CMOS Sense Amplifier-Based Flip-Flop with Two N-C ² MOS Output Latches," Electronics Letters, Vol. 36, No. 6, pp. 498-500, 2000.
	3.	"MOS Operational Amplifiers," Chapter Four, pp. 121-126, undated. *

* Note: Not considered because undated.

Examiner

HAI L. NGUYEN

Date Considered

04/18/02

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.